## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Art Unit:

Not assigned

Yoshiharu OGATA

Examiner: Not assigned

Serial No: Not assigned

Filed: March 29, 2004

For:

Semiconductor Device, Electronic Device, Electronic Appliance,

and Method of Manufacturing a Semiconductor Device

## TRANSMITTAL OF INFORMATION DISCLOSURE **STATEMENT**

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The information disclosure statement submitted herewith is being filed concurrently with the subject application [37 C.F.R. § 1.97(b)] and contains no items of information cited in any communication from a foreign patent office in a counterpart foreign application [37 C.F.R. § 1.97(e)(1)].

If it should be determined that for any reason either an insufficient or excessive fee has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 50-1314. A copy of this paper is enclosed.

Respectfully submitted,

Date: March 29, 2004

Anthony J. Orler

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**HOGAN** 

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\\\LA - 72761/0300 - 198757 v1

FORM PTO-1449			Docket Number (Optional) 81754.0120		Application Number Not assigned	
INFORMATION DISCLOSURE CITATION IN AN APPLICATION			Applicant Yoshiharu OGATA			
(Use several sheets if necessary)			Filing Date March 29, 2004		Group Art Unit Not assigned	
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	US 2001/0013643 A1	08/16/01	Nakanishi et al.			
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FOREIGN PATENT DOCUMENTS						
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION (YES/NO)
	2000-101016	04/07/00	Japan			Abstract
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)						
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